



K. J. Somaiya College of Engineering, Mumbai-77
(Autonomous College Affiliated to University of Mumbai)

SECOND HALF - 2019

T.E. (SEM. V) ESE THEORY EXAMINATION TIME TABLE

(MU CBGS Syllabus Under Autonomy with Equivalent courses from KJSCE2014)

Candidates appearing for the above said examination are requested to be present at the examination hall, fifteen minutes before the commencement of the examinations and will not be allowed after the half an hour of the commencement of the examinations. The candidates are required to carry the hall ticket / college Identity Card. THEY ARE FORBIDDEN TO CARRY ANY ELECTRONICS GADGETS LIKE MOBILE PHONE, SMART WATCH, ETC., ANY BOOKS, NOTES OR PAPER ETC. IN THE EXAMINATION HALL. Seat numbers will be displayed on the college notice boards before the date of commencement of the examination and seating arrangement will be displayed on college notice boards on the day of examination. The ESE examinations of T.E. (SEM – V) will be conducted as per the following schedule: -

DAY	WEDNESDAY	FRIDAY	MONDAY	WEDNESDAY	SATURDAY
DATE	27 November 2019	29 November 2019	2 December 2019	4 December 2019	7 December 2019
TIME	2.30-5.30 PM	2.30-5.30 PM	2.30-5.30 PM	2.30-5.30 PM	2.30-5.30 PM
COMPUTER ENGINEERING	OPERATING SYSTEMS <i>Equivalent for Operating Systems</i>	DATA NETWORKS <i>Equivalent for Computer Networks</i>	--	--	SOFTWARE ENGINEERING <i>Equivalent for Structured and Object Oriented Analysis and Design</i>
ELECTRONICS ENGINEERING	ELECTROMAGNETIC ENGINEERING <i>Equivalent for Electromagnetic Engineering</i>	SIGNALS AND SYSTEMS <i>Equivalent for Signals and Systems</i>	LINEAR INTEGRATED CIRCUIT DESIGN <i>Equivalent for Design With Linear Integrated Circuits</i>	DIGITAL COMMUNICATION AND CODING TECHNIQUES <i>Equivalent for Digital Communication</i>	<i>Microcontrollers and Applications</i>
ELECTRONICS AND TELECOMMUNICATION ENGINEERING	COMMUNICATION SYSTEMS <i>Equivalent for Analog Communication</i>	RANDOM SIGNAL AND PROCESSES <i>Equivalent for Random Signal Analysis</i>	RF FILTERS AND ANTENNAS <i>Equivalent for RF Modelling and Antennas</i>	MICROPROCESSOR AND MICROCONTROLLER <i>Equivalent for Microcontroller and Applications</i>	INTEGRATED CIRCUITS <i>Equivalent for Integrated Circuits</i>

Candidates are permitted to use the scientific but not programmable calculator during the examination. Candidates for the examination are expected to provide themselves with Data Books and drawing instruments if required. The exchange or loan of Data Books, drawing instruments or any other materials used in the examination is not permitted while the examination is in progress. Candidates must bring their own instruments and will not be allowed to borrow from each other under any circumstances. Any unfair means in the examination hall during the theory examination is punishable as per the rules and regulations.

Mumbai – 400 077
DATE: - 24 October 2019


25/10/19
PRINCIPAL



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INFORMATION TECHNOLOGY	--	<i>Open Source Technologies</i>	ADVANCED DATABASES <i>Equivalent for Advanced Database Management Systems</i>	COMPUTER GRAPHICS <i>Equivalent for Computer Graphics and Virtual Reality</i>	OPERATING SYSTEMS <i>Equivalent for Operating Systems</i>
MECHANICAL ENGINEERING	HEAT AND MASS TRANSFER <i>Equivalent for Heat Transfer</i>	THEORY OF MACHINES-II <i>Equivalent for Theory of Machines-II</i>	--	<i>Mechanical Measurements and Control</i>	PRODUCTION PROCESS-III <i>Equivalent for Production Process-III</i>

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DAY	TUESDAY
DATE	10 Dec 2019
TIME	2.30PM-5.30 PM
COMPUTER ENGINEERING	Microprocessor (of Semester IV) <i>Equivalent for Microprocessor</i>

DAY	FRIDAY
DATE	13 Dec 2019
TIME	2.30PM-5.30 PM
INFORMATION TECHNOLOGY	Microcontroller and Embedded Systems (of Semester VI) <i>Equivalent for Microcontrollers and Embedded Systems</i>

DAY	WEDNESDAY
DATE	11 Dec 2019
TIME	2.30PM-5.30 PM
MECHANICAL ENGINEERING	Energy Conversion I (of Semester VI) <i>Equivalent for IC Engines</i>

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