

K.J. Somaiya Polytechnic Mumbai – 77
Institute level Admission schedule 2024-25

Admission Round I

**Diploma in Computer/Electrical/Industrial Electronics/Mechanical
Engineering**

Sr. No.	Schedule	Time
1.	Display of Provisional Merit List	11 July 2024 after 12:00 PM
2.	Display of Final Merit List	12 July 2024 after 12:00 PM
3.	Admission Round I (Computer Engg. Branch)	15 July 2024 after 11:30 AM
4.	Admission Round I (Electrical/Industrial Electronics/Mechanical Engg. Branches)	16 July 2024 after 11:30 AM

All students whose name is appeared in final merit list are required to carry following documents along with them.

- 1) DTE Registration confirmation copy
- 2) Somaiya Registration confirmation verified copy
- 3) Original SSC mark sheet (10th)
- 4) Original Leaving/Transfer certificate (CBSC Board Migration certificate)
- 5) DD in favour of "Principal secretary K. J. Somaiya Polytechnic Mumbai"

b) Fee shall be displayed before admission round.

Note: Only first 20 students in the merit list are required to bring DD

B. A. D. S.